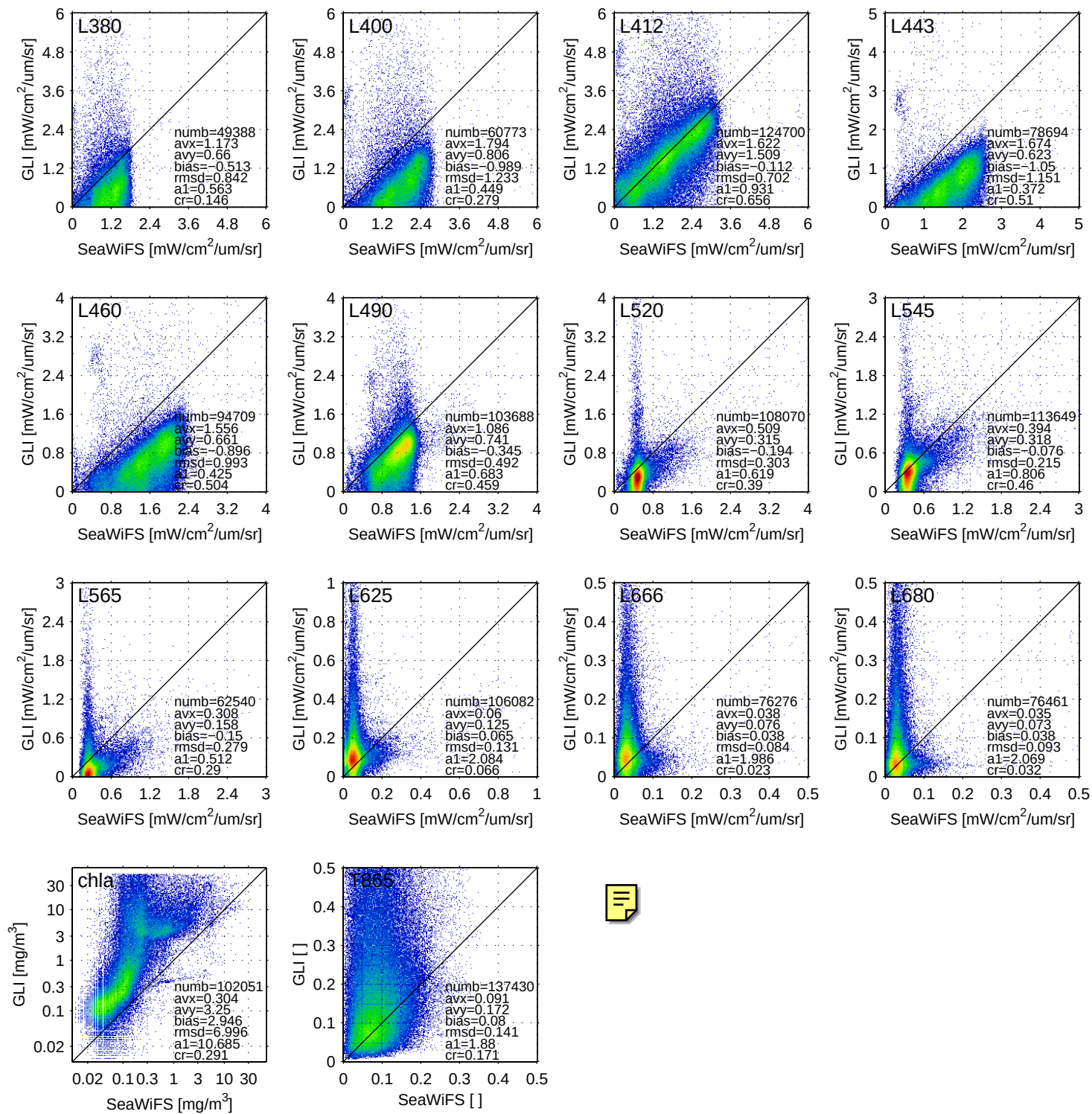
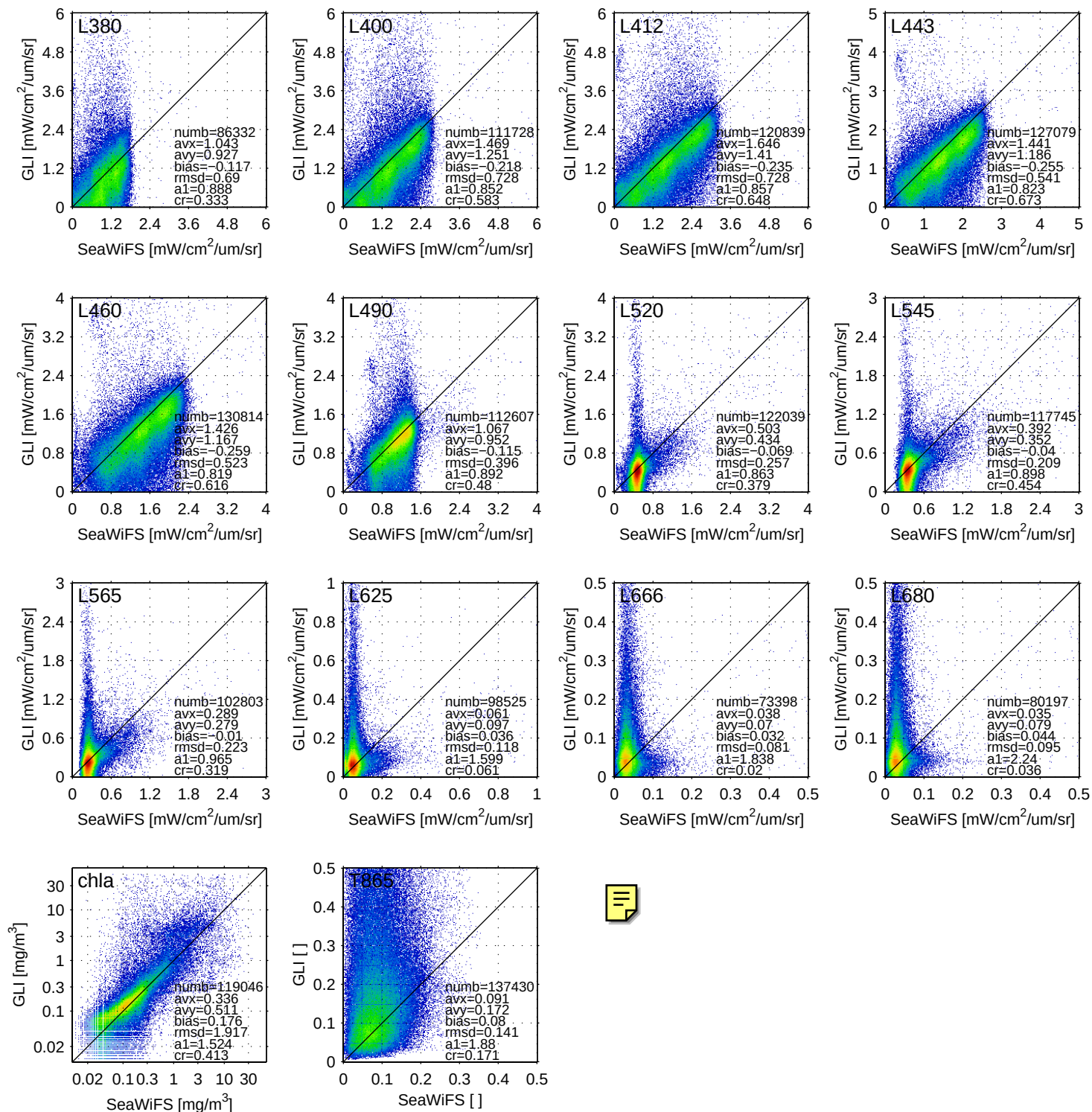


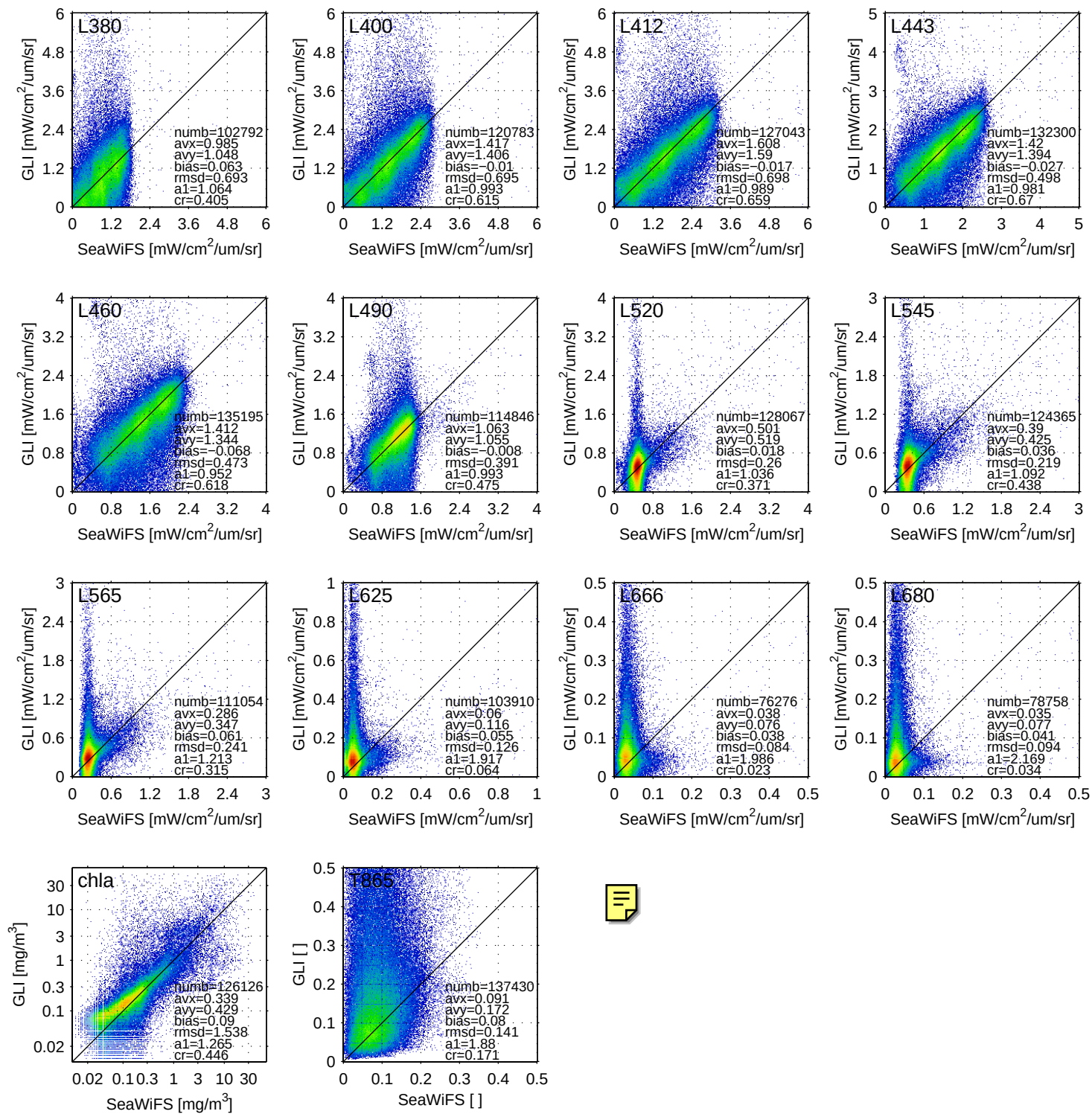
Input=../L2G0625-gmCb-t1-result.schist



Input=../L2G0625-gmCb-t2-result.schist

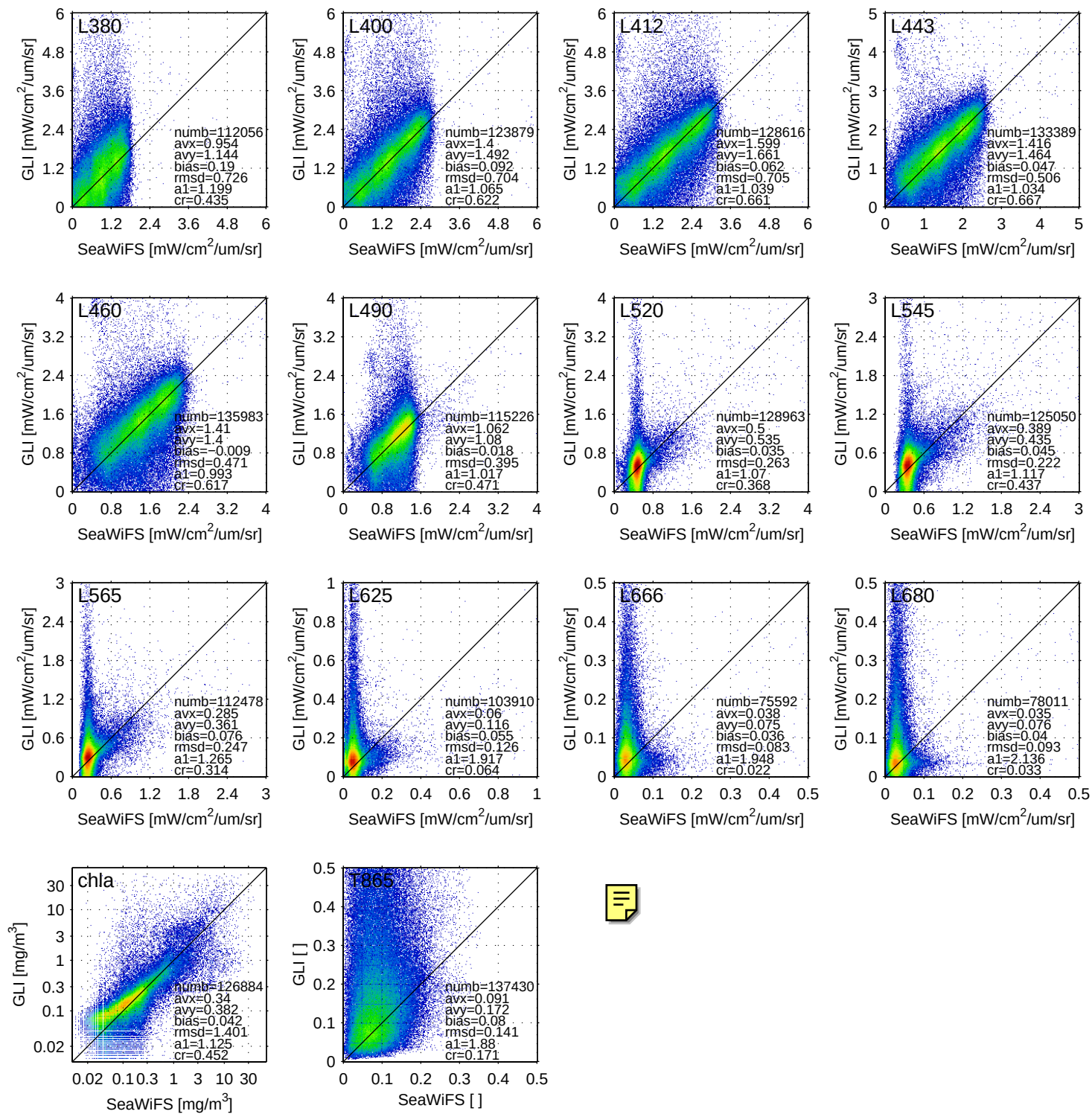


Input=../L2G0625-gmCb-t3-result.schist

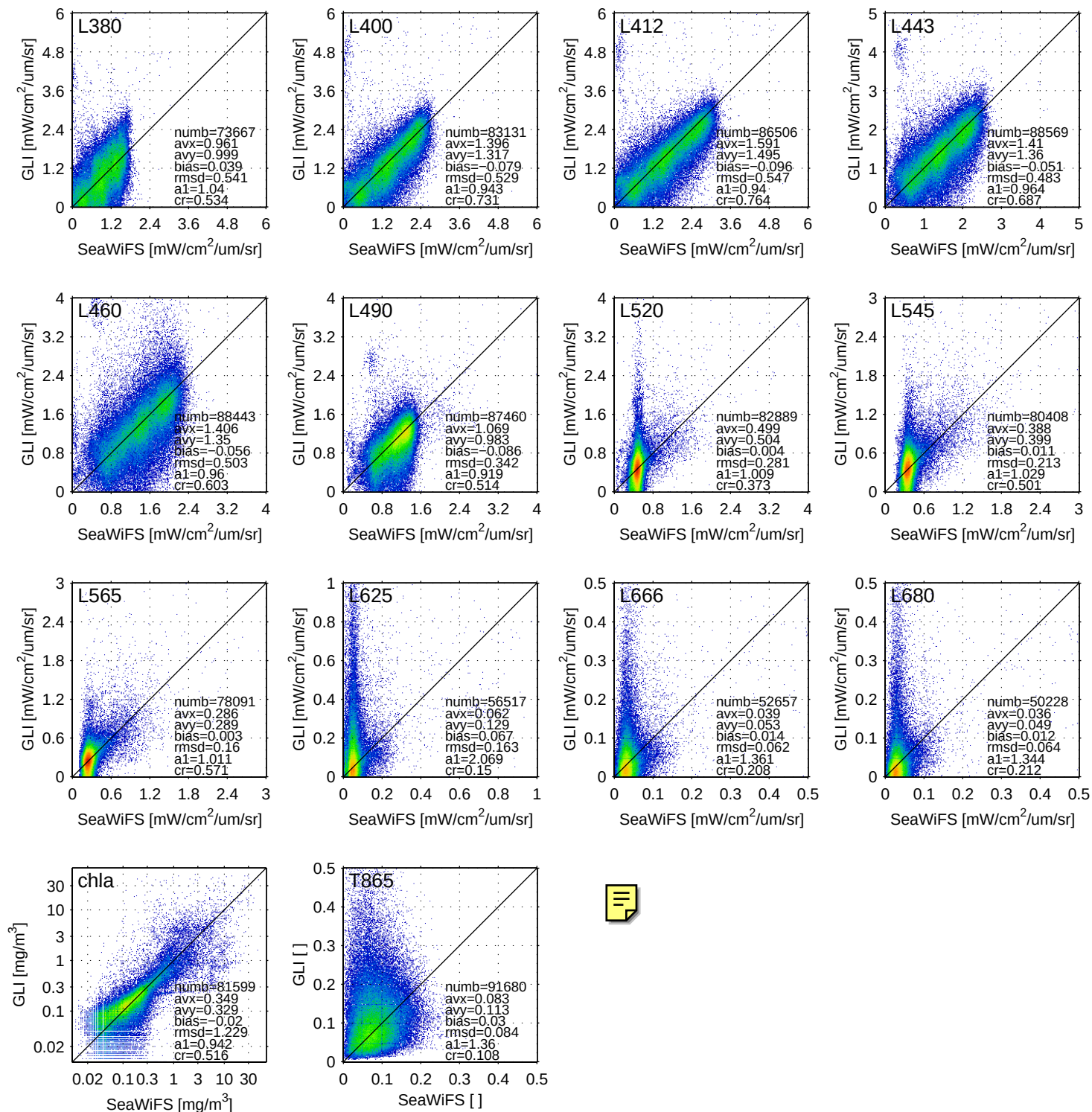




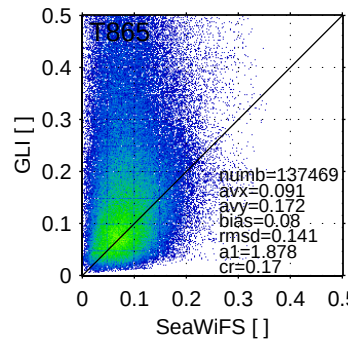
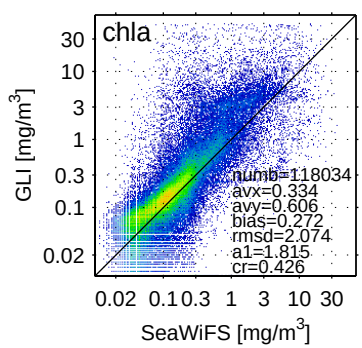
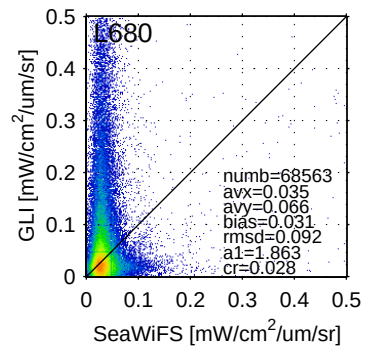
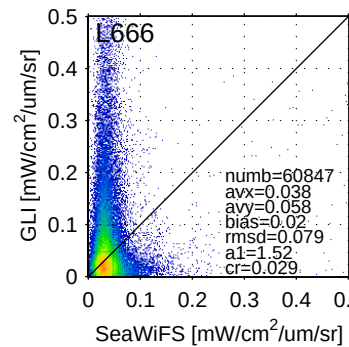
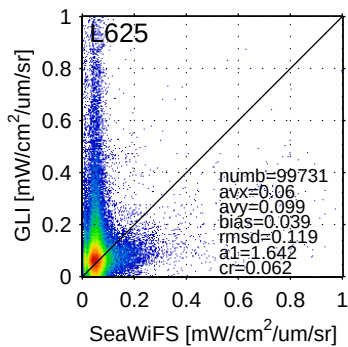
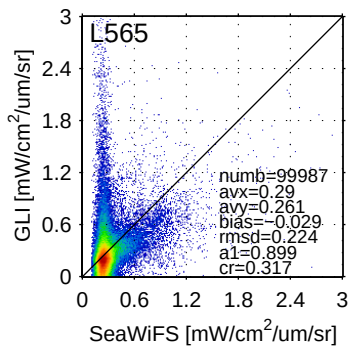
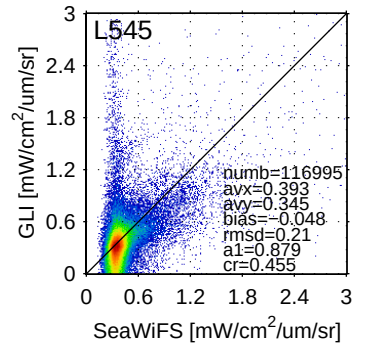
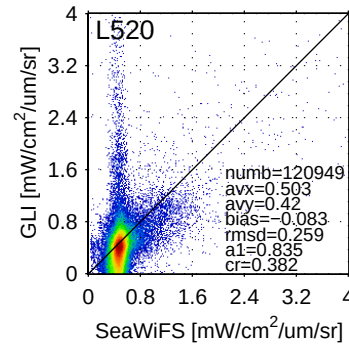
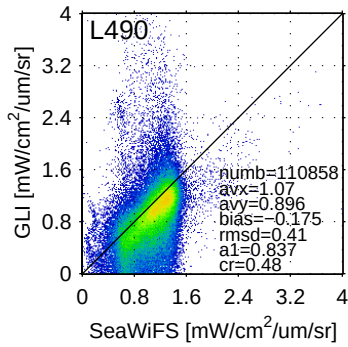
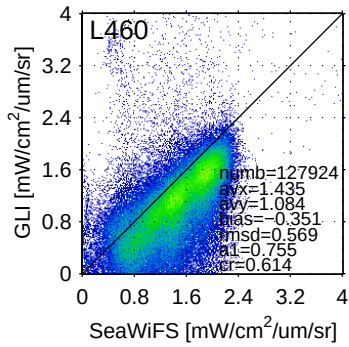
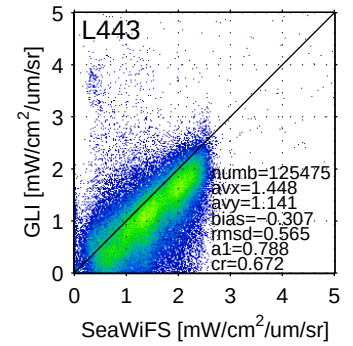
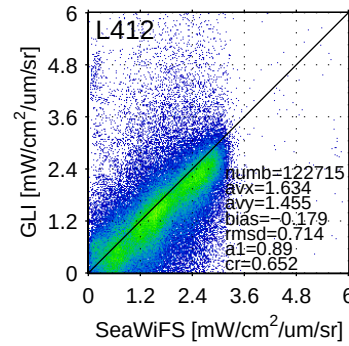
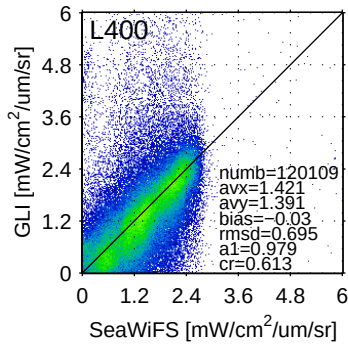
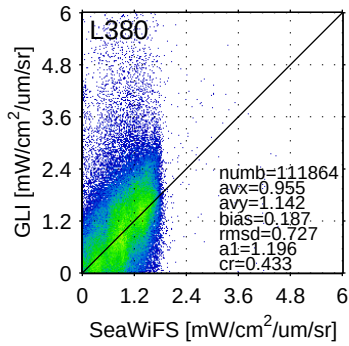
Input=../L2G0625-gmCb-t4-result.schist



Input=../L2G0625-gmCa-t4-result.schist

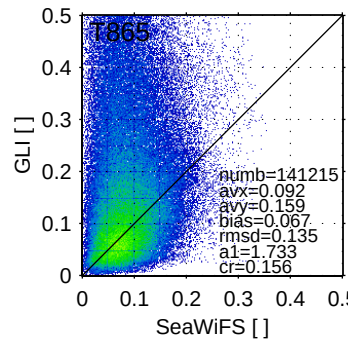
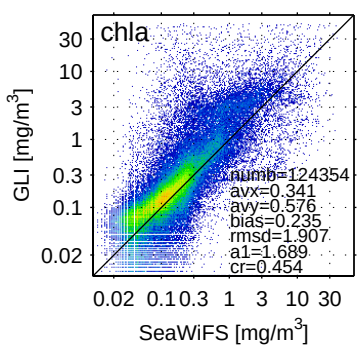
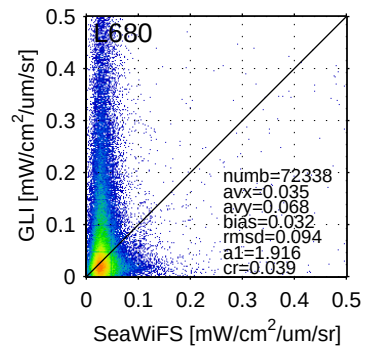
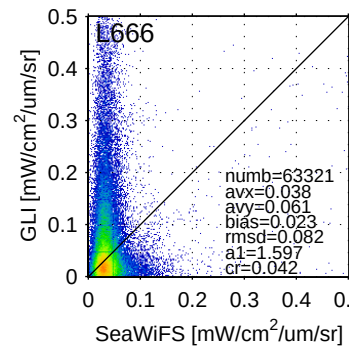
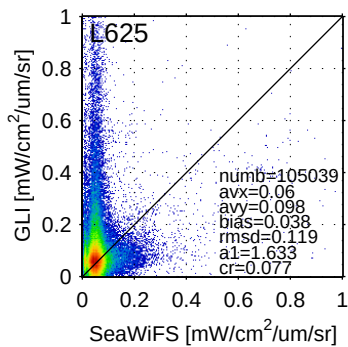
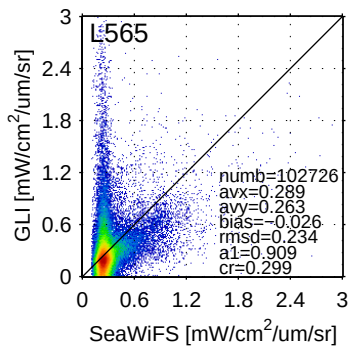
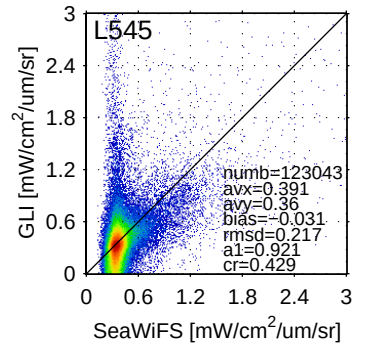
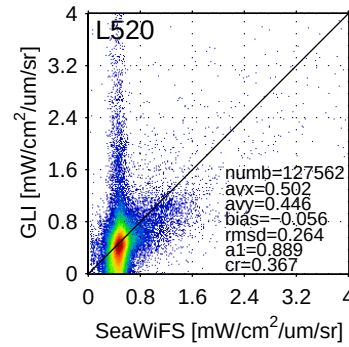
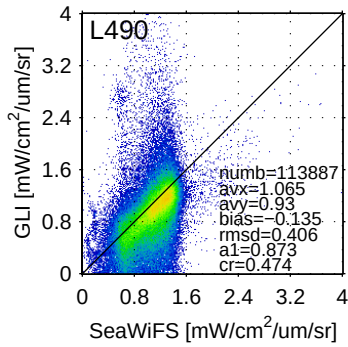
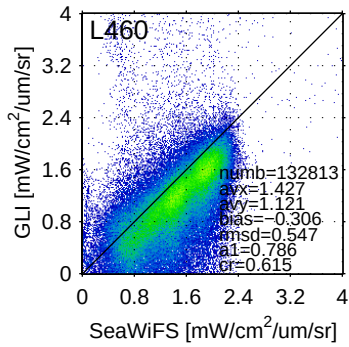
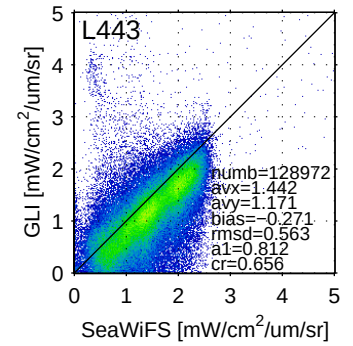
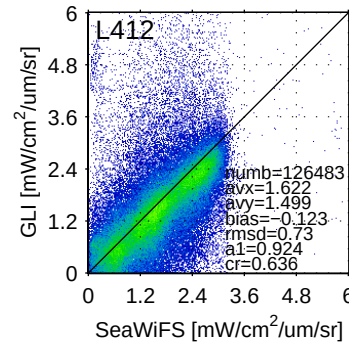
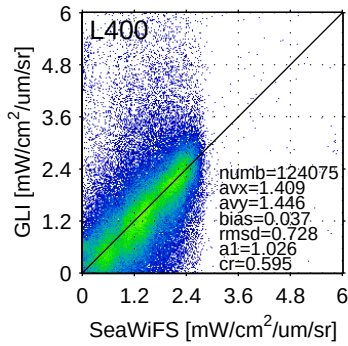
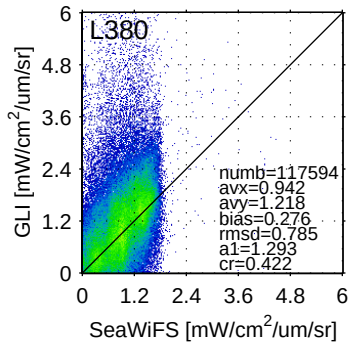


Input=../L2G0625-gmCb-t5-result.schist

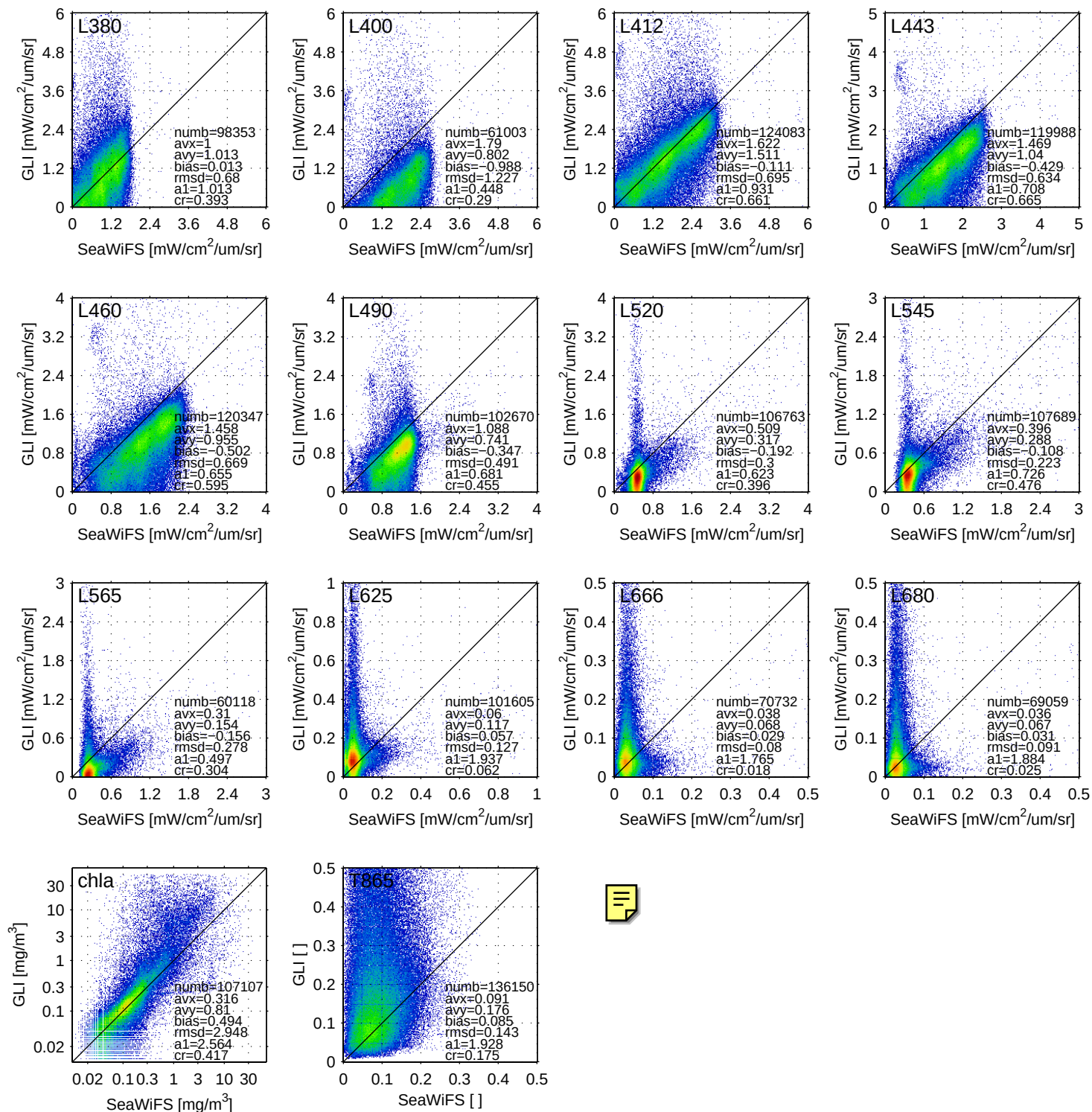




Input=../L2G0625-gmCb-t6-result.schist



Input=../L2G0625-gmCb-t7-result.schist





Input=../L2G0625-gmCb-t8-result.schist

